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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/893,994	06/29/2001	Jong Jin Park	49128-5019	5674
9629	7590	12/15/2005		
			EXAMINER	
			NGUYEN, JENNIFER T	
			ART UNIT	PAPER NUMBER
			2674	

DATE MAILED: 12/15/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	09/893,994	PARK ET AL.	
	Examiner Jennifer T. Nguyen	Art Unit 2674	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 07 December 2004.  
 2a) This action is FINAL.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 2,3 and 5-22 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) 5-17 is/are allowed.  
 6) Claim(s) 2,3 and 18-22 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

## DETAILED ACTION

1. This Office action is responsive to amendment filed 9/29/05.

### *Claim Rejections - 35 USC § 103*

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 2, 3, and 18-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nose et al. (U.S. Patent No. 6,819,311) in view of Applicant Admitted Prior Art (hereinafter AAPA) fig. 3.

Regarding claims 2 and 3, referring to Figs. 1 and 12, Nose teaches a method of driving a liquid crystal display including a liquid crystal display panel (1) having pixels (5) arranged in a matrix form, a gate driver for applying a scanning signal to gate lines (G1-Gn) of the liquid crystal display panel, and a data driver for supplying a picture data to data lines (D1-Dm) of the liquid crystal display panel, the method comprising the steps of:

applying a clock pulse (Vclk) to the gate driver;  
applying gate output enable signals (OE) to the gate driver; and  
applying a scanning pulse to two gate lines (G1, G2) during one period of the clock pulse (t3) wherein the data driver supplies the picture data (t1) to the data lines when the scanning pulse is applied to a first gate line of the two gate lines, and supplies a black data (t2) to the data lines when the scanning pulse is applied to a second gate line of the two gate lines (col. 8, line 42 to col. 9, line 7).

Nose differs from claims 2 and 3 in that he does not specifically teaches applying first to third gate output enable signals to the gate driver.

AAPA fig. 3 discloses applying first to third gate output enable signals to the gate driver [0007] of support specification. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the applying first to third gate output enable signals to the gate driver as taught by AAPA in the system of Nose in order to prevent a crosstalk phenomenon between pixels of display.

Regarding claims 18-20, referring to Figs. 1 and 12, Nose teaches a method for driving a liquid crystal display panel (1), comprising:

selecting two gate lines (G1, G2) that are separated by a predetermined number of gate lines based on received gate output enable signals;

providing picture signals (t1) to a row of pixels corresponding to one of the two selected gate lines;

providing a reference signal (t2) to a row of pixels corresponding to the other one of the two selected gates lines;

repeating for different pairs of gate lines (Gj, Gj+1)so that all rows of pixels are refreshed by corresponding picture signals in one frame; wherein each frame so that updated picture signals are provided to the pixels that bear the reference signal immediately prior to being updated (col. 8, line 42 to col. 9, line 7).

Nose differs from claims 18-20 in that he does not specifically teaches based on received first to third gate output enable signals.

AAPA fig. 3 discloses selecting the gate lines based on received first to third gate output enable signals [0008]-[0010] of support specification. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the selecting the gate lines based on received first to third gate output enable signals as taught by AAPA in the system of Nose in order to prevent a crosstalk phenomenon between pixels of display.

Regarding claims 21-22, Nose teaches a horizontal band of pixels applied with the reference signal sweeps an entire screen of the liquid crystal display panel from the top to the bottom in each frame (col. 8, line 42 to col. 9, line 7).

4. Claims 5-17 are allowed.

5. Applicant's arguments with respect to claims 2, 3, 11, 12, and 18-22 have been considered but are moot in view of the new ground(s) of rejection.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer T. Nguyen whose telephone number is 571-272-7696. The examiner can normally be reached on Mon-Fri: 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Patrick N. Edouard can be reached on 571-272-7603. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jennifer Nguyen  
12/7/05



PATRICK N. EDOUARD  
SUPERVISORY PATENT EXAMINER